

IN THE CLAIMS:

Please amend Claims 1, 13, 15, 17, 18 and 22 as follows.

1. (Currently Amended) A display device comprising:

a display panel having cathodes, gates, and ~~anodes~~ an anode in which said cathodes and said gates are connected in a matrix manner;

an electron emitter which emits electrons in a state where a voltage is applied only between said cathodes and said ~~anodes~~ anode and which is provided with said cathode;

in which display of a plurality of pixels in said display panel is performed under a dark state by stopping electron emission from said electron emitter for said ~~anodes~~ anode by applying a stop voltage between said cathodes and said gates, and

control means which controls operation of a circuit for driving a display panel in such a way that a potential of ~~each of said anodes~~ anode becomes not less than a threshold value potential by which a potential of ~~each of said anodes~~ anode can perform electron emission from said electron emitter after a predetermined time elapse from starting of application of a drive voltage which provides a specified display state or the stop voltage between said cathodes and said gates when a display starting signal is generated.

2. (Previously Presented) The display device according to claim 1, wherein application of the stop voltage between said cathodes and said gates and the drive

voltage which provides the specified display state is performed for all of said pixels of said display panel at the same time.

3. (Previously Presented) The display device according to claim 1, wherein a scanning selective potential is supplied to at least one line scanning wiring in said display panel, a scanning non-selective potential is supplied to remaining scanning wirings in said display panel, a predetermined potential which generates the darkest state for each pixel is supplied to all columns of said display panel in synchronization with supply of the scanning selective potential or a predetermined modulation potential is supplied, whereby the stop voltage or the drive voltage which provides the specified state is applied between said cathodes and said gates.

4. (Previously Presented) The display device according to claim 1, wherein said display panel drive circuit has:

- an anode power supply circuit for supplying the anode potential;
- a cathode drive circuit for driving said cathodes;
- a gate drive circuit for driving said gates; and
- a drive power supply circuit for supplying a driving reference voltage

for generating a drive voltage which provides the stop voltage or the specified display state to said cathode drive circuit and said gate drive circuit.

5. (Previously Presented) The display device according to claim 4, wherein said drive power supply circuit starts supplying the driving reference voltage under a state where a drive potential for a logic circuit to said cathode drive circuit and said gate drive circuit, supply of the driving reference voltage starts, and thereafter said cathode drive circuit and said gate drive circuit start application of the stop voltage or the driving voltage which provides the specified display state.

6. (Previously Presented) The display device according to claim 4, wherein a terminal when application of the stop voltage or the driving voltage provides the specified display state, said anode power supply circuit holds said anode so as to have a specified potential sufficiently lower than the threshold value potential which emits electrons from said electron emitter under a state where a drive potential for a logic circuit is supplied to said anode power supply circuit.

7. (Previously Presented) The display device according to claim 4, wherein after application of the stop voltage or the drive voltage which provides the specified display state starts, application of drive voltage is permitted based on input display image data from said cathode drive circuit and said gate drive circuit to said display panel.

8. (Previously Presented) The display device according to claim 1, wherein after the voltage between said cathodes and said gates is transited from an unstable state

to zero, application of the stop voltage and the drive voltage which provides the specified display state starts.

9. (Previously Presented) The display device according to claim 1, wherein any one of first and second supply steps is performed, said first step of supplying scanning non-selective potential which applies the stop voltage irrespective of a potential of the other wiring to any one of cathode wirings and gate wirings which are defined by scanning wirings of said display panel; and

said second step of supplying a modulation potential which applies a drive voltage that provides the specified display state or the stop voltage irrespective of a potential of the other wiring defined by said scanning wirings to any one of said cathode wirings and said gate wirings which are defined by modulation signal wirings,

whereby the stop voltage or the drive voltage which provides the specified display state is applied between said cathodes and said gates.

10. (Previously Presented) The display device according to claim 1, wherein a modulation potential to be supplied to any one of ~~said~~ cathode wiring and ~~said~~ gate wiring which are defined by modulation signal wirings of said display panel is a potential selected from three or more levels, and potentials at two or more levels from among them are potentials which generate a drive voltage which may emit electrons by allowing said two or more

level potentials to be supplied in synchronization with said scanning selective potential, and one of them is a potential which generates the stop voltage.

Claim 11. (Cancelled).

12. (Previously Presented) The display device according to claim 1, wherein said emitter is at least one kind selected from carbon nanotube, graphite nanofiber, amorphous carbon, carbon nonohorne, graphite, diamond like carbon, diamond and fullerene.

13. (Currently Amended) A drive control method for a display device having a display panel having cathodes, gates, and ~~anodes~~ an anode in which said cathodes and said gates are connected in a matrix manner, and an electron emitter which emits electrons in a state where a voltage is applied only between said cathodes and said ~~anodes~~ anode and which is provided with said cathode, in which display of pixels of said display panel is performed under a dark state by stopping electron emission from said electron emitter for said ~~anodes~~ anode by applying a stop voltage between said cathodes and said gates,

said method comprising:

an application step of applying the stop voltage or a drive voltage which provides a specified display state between said cathodes and said gates when a display starting signal is generated; and

an anode voltage supplying step of allowing a potential of said anode not to be less than a threshold value potential which emits electrons from said electron emitter after a predetermined time elapse from starting of said application step.

14. (Previously Presented) The drive control method for the drive device according to claim 13, wherein

a driving power supply circuit starts supplying a driving reference potential for generating the stop voltage or a drive voltage which provides the specified display state to a cathode drive circuit and a gate drive circuit under a state where a drive potential for a logic circuit is supplied to a cathode drive circuit and a gate drive circuit;

thereafter, said application step starts, and at a starting time of said application step, an anode power supply circuit holds said anode at a specified voltage which is sufficiently lower than a threshold voltage which may perform electron emission from said electron emitter under a state where the drive potential for said logic circuit is supplied to an anode power supply circuit; and

further thereafter, said anode voltage supplying step starts, said anode is held at a voltage which is sufficiently higher than the threshold potential which performs electron emission from said electron emitter, and application of the drive voltage for display is permitted based on a display image input to said display panel from said cathode drive circuit and said gate drive circuit.

15. (Currently Amended) A display device comprising:

a display panel having cathodes, gates, and ~~anodes~~ an anode respectively connected to a cathode driving circuit, a gate driving circuit, and an anode power source circuit, in which said cathodes and said gates are connected in a matrix manner; an electron emitter which emits electrons in a state where a voltage is applied only between said cathode and said anode, said electron emitter being provided with said cathode, in which display of a plurality of pixels in said display panel is performed under a dark state by stopping electron emission from said electron emitter to said ~~anodes~~ anode by applying a stop voltage between said cathodes and said gates; and

a control circuit which controls said cathode driving circuit, said gate driving circuit, and said anode power source circuit,

wherein said control circuit outputs a display starting signal to said cathode driving circuit, said gate driving circuit, and said anode power source circuit, and controls operations of circuits for driving the display panel in such a way that a potential of ~~each~~ of said ~~anodes~~ anode becomes not less than a threshold value potential by which said potential of ~~each of~~ said ~~anodes~~ anode can perform electron emission from said electron emitter after a predetermined time elapse from starting of application of a drive voltage which provides a specified display state or the stop voltage between said cathodes and said gates after a predetermined time elapse from a time at which the display starting signal is outputted after potentials of said cathode and said gate are kept at a same potential.

16. (Previously Presented) The display device according to claim 15, wherein said electron emitter includes at least one kind selected from carbon nanotube, graphite nanofiber, amorphous carbon, carbon nanohorn, graphite, diamond like carbon, diamond and fullerene.

17. (Currently Amended) A drive control method of a display device having a display panel having cathodes, gates, and ~~anodes~~ an anode in which said cathodes and said gates are connected in a matrix manner; an electron emitter which emits electrons in a state where a voltage is applied only between said cathodes and said ~~anodes~~ anode, said electron emitter being provided with said cathode, in which display of a plurality of pixels in said display panel is performed under a dark state by stopping electron emission from said electron emitter to said ~~anodes~~ anode by applying a stop voltage between said cathodes and said gates, said method comprising:

a step of generating a display starting signal;

an application step of applying a drive voltage which provides said stop voltage or a specified display state between said cathodes and said gates after a predetermined time elapse from a time at which the display starting signal is generated after potentials of said cathode and said gates are kept at a same potential; and

an anode potential supplying step of allowing a potential of ~~each of~~ said ~~anodes~~ anode not to be less than a threshold value potential which emits electrons from said electron emitter after a predetermined time elapse from starting of said application step.



18. (Currently Amended) A display device comprising:

a display panel having cathodes, gates, and ~~anodes~~ an anode respectively connected to a cathode driving circuit, a gate driving circuit, and an anode power source circuit, in which said cathodes and said gates are connected in a matrix manner; an electron emitter which emits electrons in a state where a voltage is applied only between said cathode and said anode, said electron emitter being provided with said cathode, in which display of a plurality of pixels in said display panel is performed under a dark state by stopping electron emission from said electron emitter to said ~~anodes~~ anode by applying a stop voltage that a potential of each of said gates is lower than a potential of each of said cathodes between said cathodes and said gates; and

a control circuit which controls said cathode driving circuit, said gate driving circuit, and said anode power source circuit,

wherein said control circuit outputs a display starting signal to said cathode driving circuit, said gate driving circuit, and said anode power source circuit, and controls operations of circuits for driving the display panel in such a way that a potential of ~~each~~ of said ~~anodes~~ anode becomes not less than a threshold value potential necessary to perform electron emission from said electron emitter by the potential of ~~each of said anodes~~ anode after a predetermined time elapse from starting of application of the stop voltage between said cathodes and said gates of all pixels of said display panel at the same time after a predetermined time elapse from a time at which the display starting signal is outputted after potentials of said cathode and said gate are kept at a same potential.

19. (Previously Presented) The display device according to claim 18, wherein a scanning selective potential is supplied to at least one line scanning wiring of said display panel, a scanning non-selective potential is supplied to remaining scanning wirings, said stop voltage is applied between said cathodes and said gates by supplying a modulation potential which generates the darkest state for each pixel to all modulation signal wirings of said display panel in synchronization with supply of said scanning selective potential.

20. (Previously Presented) The display device according to claim 18, wherein after the voltage between cathodes and gates is transited from an unstable state to zero, application of the stop voltage starts.

21. (Previously Presented) The display device according to claim 18, wherein said electron emitter includes at least one kind selected from carbon nanotube, graphite nanofiber, amorphous carbon, carbon nonohorne, graphite, diamond like carbon, diamond and fullerene.

22. (Currently Amended) A drive control method of a display device having a display panel having cathodes, gates, and ~~anodes~~ an anode, in which said cathodes and said gates are connected in a matrix manner; an electron emitter which emits electrons in a state where a voltage is applied only between said cathode and said anode, said electron emitter being provided with said cathode, in which display of each of pixels is performed under a dark state by

stopping electron emission from said electron emitter to said ~~anodes~~ anode by applying a stop voltage that a potential of each of said gates is lower than a potential of each of said cathodes between and said gates, said method comprising:

a step of generating a display starting signal;

an application step of applying a stop voltage between said cathodes and said gates of all pixels of said display panel at the same time after a predetermined time elapse from a time at which the display starting signal is generated after potentials of said cathode and said gate are kept at a same potential; and

an anode potential supplying step of allowing a potential of said ~~anodes~~ anode not to be less than a threshold value potential necessary to perform electron emission from said electron emitter after a predetermined time elapse from starting of said application step.